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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Yoichi MIURA

Group Art Unit: 2812

Application No.: 10/527,169

Docket No.: 123739

Filed: March 9, 2005

For: WIRING BOARD AND METHOD OF MANUFACTURING THE SAME

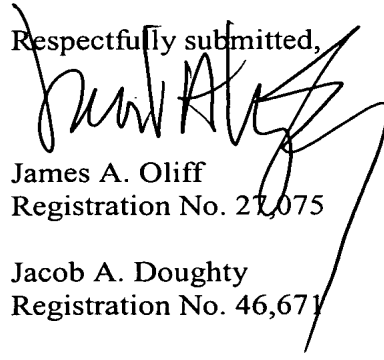
REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
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Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,


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JAO:JAD/tje

Date: March 16, 2006

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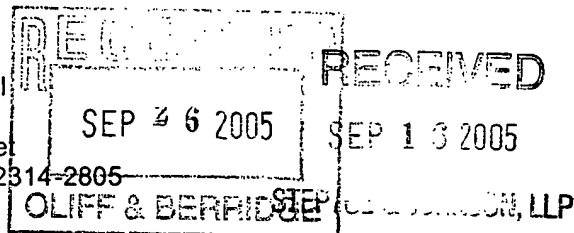


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/527,169	03/09/2005	2812	900	DAIN:802	6	11	3

Parkhurst Wendel
Suite 210
1421 Prince Street
Alexandria, VA 22314-2805



CONFIRMATION NO. 9281

FILING RECEIPT



OC000000016893881

Date Mailed: 09/06/2005

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Assignment for Published
Patent Application
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Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/04908 04/05/2004

Foreign Applications

2003-105794

JAPAN 2003-10594 04/09/2003

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Projected Publication Date: 12/08/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method of Manufacturing

Wiring board and process for producing the same

^

Preliminary Class

438

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